



Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>	
				Application Number	10/674,835
				Filing Date	September 29, 2003
				First Named Inventor	Jose S. Niell
				Art Unit	
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	42P17022

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
/OFR/		HNATEK, EUGENE R., "A User's Handbook of Semiconductor Memories", 1977, A Wiley-Interscience Publication, John Wiley & Sons, Inc., Pgs. 163-181		
/OFR/		TAUB, HERBERT et al., "Digital Integrated Electronics", 1977, McGraw-Hill Book Company, Pgs. 394-396		
/OFR/		MANO, M. MORRIS, "Computer System Architecture, Second Edition", 1982, Prentice-Hall, Inc., Pgs. 426-428		
/OFR/		HOROTTITZ, PAUL et al., "The Art of Electronics, Second Edition", 1989, Cambridge University Press, Pg. 541		
/OFR/		"CMOS BiCMOS Data Book", Cypress Semiconductor Corporation, February 1, 1989, Pgs. 5-34 through 5-57		
/OFR/		"Databook Data Converters - DSP Products", TRW LSI Products Inc., 1990, Pgs. 23-52		
/OFR/		DORF, RICHARD C., "The Electrical Engineering Handbook", 1993, CRC Press, Inc., Pgs. 1725-1727		

Examiner Signature	/Omar Fernandez Rivas/	Date Considered	5/24/2007
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zalman (w/r) 08/11/2003.  
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